

CITY, DOCKET NO.
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TAKAHASHI et al.FILING DATE
December 2, 1997GROUP
2511

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	4,658,377	4/1987	McElroy	365	51	
	4,590,588	5/1986	Itoh et al.			
	5,301,142	4/1994	Suzuki et al.			
	5,297,102	3/1994	Tanizaki			
	5,404,338	4/1995	Murai et al.			
	5,140,550	8/1992	Miyaoka et al.			
	5,282,175	1/1994	Fujita et al.			
	5,274,595	12/1993	Seok et al.			
RECEIVED						
JUN 05 1998						
GROUP 2100						

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	YES	NO
	JP-A-5-54634	3/5/93	Japan					
	JP-A-1-245489	9/29/89	Japan					
	JP-A-2-158995	6/19/90	Japan					
	JP-A-2-18785	1/23/90	Japan					
	JP-A-4-59712	9/24/92	Japan					

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

T. Sugibayashi et al., "A 30ns 256Mb DRAM with Multi-Divided Array

Structure", 1993 IEEE Int'l Solid-State Circuits Conference, pp. 50-51.

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